1 The convex C240 architecture

M. Chastain, G. Gostin, J. Mankovich, S. Wallach

November 1988 **Supercomputing '88:** Proceedings of the 1988 ACM/IEEE

Publisher: IEEE Computer Society Press

Full text available: Pdf (961.68 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 17, Downloa

The C240, a tightly coupled, shared memory, parallel/multi-processor, s Cray-like processors. Managed by a fully semaphored UNIX operating th directly addressable physical ...

2 Space-efficient scheduling of nested parallelism

Girija J. Narlikar, Guy E. Blelloch

January 1999 **Transactions on Programming Languages and Systems**

Publisher: ACM Request Permissions

Full text available: Pdf (481.02 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 47, Downloa

Many of today's high-level parallel languages support dynamic, fine-gra user to expose all the parallelism in the program, which is typically of a processors. Hence an ...

Keywords: dynamic scheduling, multithreading, nested parallelism, pa efficiency

3 An adaptive resource partitioning algorithm for SMT processors

Huaping Wang, Israel Koren, C. Mani Krishna

October 2008 **PACT '08:** Proceedings of the 17th international conference c techniques

Publisher: ACM Request Permissions

Full text available: Pdf (523.87 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 67, Downloa

Simultaneous Multithreading (SMT) increases processor throughput by a threads. However, fully sharing processor resources may cause resourc misallocations, resulting ...

Keywords: resource partitioning, simultaneous multithreading

4 Parallel depth first vs. work stealing schedulers on CMP architecture

Vasileios Liaskovitis, Shimin Chen, Phillip B. Gibbons, Anastassia Ailamaki, Nikos Hardavellas, Michael Kozuch, Todd C. Mowry, Chris Wilkerson

July 2006 **SPAA '06:** Proceedings of the eighteenth annual ACM sympo architectures

Publisher: ACM Request Permissions

Full text available: Pdf (71.25 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 13, Downloads (12 Months): 68, Downlo

In chip multiprocessors (CMPs), limiting the number of off-chip cache m multithreaded programs provide opportunities for *constructive cache sh* share a largely ...

Keywords: caches, chip multiprocessors, scheduling

5 Super-threading: architectural and software mechanisms for optimizi

Shuichi Sakai, Kazuaki Okamoto, Hiroshi Matsuoka, Hideo Hirono, Yuetsu K

August 1993 **ICS '93:** Proceedings of the 7th international conference on S

Publisher: ACM Request Permissions

Full text available: Pdf (913.09 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 20, Downloa

This paper presents super-threading, which generically means the archi optimizing parallel computation. Super-threading includes architectural mechanism for ...

6 Multi-processor performance on the Tera MTA

Allan Snavely, Larry Carter, Jay Boisseau, Amit Majumdar, Kang Su Gatlin,

November 1998 **Supercomputing '98:** Proceedings of the 1998 ACM/IEEE

Publisher: IEEE Computer Society

Full text available: Html (36.39 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 12, Downloa

The Tera MTA is a revolutionary commercial computer based on a multit many other parallel architectures, the Tera MTA can effectively use high By running multiple ...

Keywords: NAS parallel benchmarks, parallelizing compilers, performa analysis, tera multithreaded architecture

7 Practical escape analyses: how good are they?

Kyungwoo Lee, Xing Fang, Samuel P. Midkiff

June 2007 **VEE '07:** Proceedings of the 3rd international conference on

Publisher: ACM Request Permissions

Full text available: Pdf (626.23 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 60, Downlo

A key analysis developed for the compilation of parallel programs is *thr escape analysis*), which determines what objects are accessed in more t program ...

Keywords: Java, analysis precision, dynamic compilation, escape analy

8 A parallel, real-time garbage collector

Perry Cheng, Guy E. Blelloch

June 2001 **PLDI '01:** Proceedings of the ACM SIGPLAN 2001 conference implementation

Publisher: ACM Request Permissions

Full text available: Pdf (1.82 MB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 82, Downloa

We describe a parallel, real-time garbage collector and present experim and good real-time bounds. The collector is designed for shared-memor collector algorithm ...

Also published in:
May 2001 **SIGPLAN Notices** Volume 36 Issue 5

9 A hybrid execution model for fine-grained languages on distributed n

John Plevyak, Vijay Karamcheti, Xingbin Zhang, Andrew A. Chien

December 1995 **Supercomputing '95:** Proceedings of the 1995 ACM/IEEE

Publisher: ACM

Full text available: Html (59.68 KB), Ps (419.70 KB) Additional Information: full citati

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 10, Downloa

While fine-grained concurrent languages can naturally capture concurre their flexibility has generally resulted in poor execution effciency. In suc small threads which ...

10 Program optimization space pruning for a multithreaded gpu

Shane Ryoo, Christopher I. Rodrigues, Sam S. Stone, Sara S. Baghsorkhi, Hwu

April 2008 **CGO '08:** Proceedings of the 6th annual IEEE/ACM internatio optimization

Publisher: ACM

Full text available: Pdf (522.93 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 45, Downloads (12 Months): 287, Downl

Program optimization for highly-parallel systems has historically been c the performance tuning by hand. With the introduction of inexpensive, s developers will ...

Keywords: gpgpu, optimization, parallel computing

11

Pointer analysis for structured parallel programs
Radu Rugina, Martin C. Rinard
January 2003 **Transactions on Programming Languages and Systems**
Publisher: ACM Request Permissions
Full text available: Pdf (383.29 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 71, Downloa

This paper presents a novel interprocedural, flow-sensitive, and context multithreaded programs that may concurrently update shared pointers. with structured parallel ...

Keywords: Pointer analysis

12 Component-Based Lock Allocation
Richard L. Halpert, Christopher J. F. Pickett, Clark Verbrugge
September 2007 **PACT '07:** Proceedings of the 16th International Conferen Techniques (PACT 2007)
Publisher: IEEE Computer Society
Full text available: Pdf (252.53 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 43, Downloa

The allocation of lock objects to critical sections in concurrent programs Recent work explores automatic lock allocation, aiming primarily to min allocating locks to ...

13 Parallelizing query optimization
Wook-Shin Han, Wooseong Kwak, Jinsoo Lee, Guy M. Lohman, Volker Mark
August 2008 **Proceedings of the VLDB Endowment** , Volume 1 Issue 1
Publisher: VLDB Endowment
Full text available: Pdf (685.98 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 32, Downloads (12 Months): 178, Downl

Many commercial RDBMSs employ cost-based query optimization exploi generate the optimal query execution plan. However, optimization time than 10 tables. Randomized ...

14 ATOP-space and time adaptation for parallel and grid applications vi
Angela C. Sodan, Lin Han
October 2004 **ARM '04:** Proceedings of the 3rd workshop on Adaptive and r
Publisher: ACM
Full text available: Pdf (803.18 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 13, Downloa

Adaptive resource allocation is becoming an important feature to run pa space and time according to current workload, to schedule around obsta system load under ...

Keywords: data partitioning, job scheduling, middleware, over-partitio

15 Pointer and escape analysis for multithreaded programs

Alexandru Salcianu, Martin Rinard

July 2001 **PPoPP '01:** Proceedings of the eighth ACM SIGPLAN symposi programming

Publisher: ACM Request Permissions

Full text available: Pdf (318.11 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 81, Downlo

This paper presents a new combined pointer and escape analysis for mu new abstraction called parallel interaction graphs to analyze the interact points-to, ...

Also published in:

July 2001 **SIGPLAN Notices** Volume 36 Issue 7

16 A parallel, incremental and concurrent GC for servers

Yoav Ossia, Ori Ben-Yitzhak, Irit Goft, Elliot K. Kolodner, Victor Leikehman,

June 2002 **PLDI '02:** Proceedings of the ACM SIGPLAN 2002 Conference implementation

Publisher: ACM Request Permissions

Full text available: Pdf (231.80 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 41, Downloa

Multithreaded applications with multi-gigabyte heaps running on moder collection (GC). The challenges for "server-oriented" GC include: ensuri heap, while minimizing ...

Keywords: JVM, Java, concurrent garbage collection, garbage collectio ordering

Also published in:

May 2002 **SIGPLAN Notices** Volume 37 Issue 5

17 X10: concurrent programming for modern architectures

Vijay A. Saraswat, Vivek Sarkar, Christoph von Praun

March 2007 **PPoPP '07:** Proceedings of the 12th ACM SIGPLAN symposiu programming

Publisher: ACM Request Permissions

Full text available: Pdf (19.19 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 16, Downloads (12 Months): 155, Downl

Two major trends are converging to reshape the landscape of concurrer First, trends in modern architectures (multi-core, accelerators, high per making concurrency and distribution ...

Keywords: X10, asynchrony, concurrent programming languages, fork hierarchical parallelism, high performance computing languages, partitic

18 A compiler and runtime system for enabling data mining applications

Wenjing Ma, Gagan Agrawal
February 2009 **PPoPP '09:** Proceedings of the 14th ACM SIGPLAN symposi programming

Publisher: ACM Request Permissions

Full text available: Pdf (785.28 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 28, Downloads (12 Months): 351, Downl

With increasing need for accelerating data mining and scientific data an improve processor performance by simply increasing clock frequencies, FPGAs and GPUs ...

Keywords: cuda, data mining, gpgpu

19 High-performance IPv6 forwarding algorithm for multi-core and multi
Xianghui Hu, Xinan Tang, Bei Hua
March 2006 **PPoPP '06:** Proceedings of the eleventh ACM SIGPLAN symp programming

Publisher: ACM Request Permissions

Full text available: Pdf (555.50 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 113, Downlo

IP forwarding is one of the main bottlenecks in Internet backbone route match at 10Gbps speed or higher. IPv6 forwarding further exacerbates quadrupled. We propose ...

Keywords: IPv6 forwarding, multithreading, network processor, paralle thread-level parallelism

20 Parallel generational-copying garbage collection with a block-structu
Simon Marlow, Tim Harris, Roshan P. James, Simon Peyton Jones
June 2008 **ISMM '08:** Proceedings of the 7th international symposium o

Publisher: ACM Request Permissions

Full text available: Pdf (598.90 KB) Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 52, Downloa

We present a parallel generational-copying garbage collector implement a block-structured memory allocator, which provides a natural granulari threads, leading to a ...

Keywords: parallel garbage collection

Result page: **1**